

FIG. 2

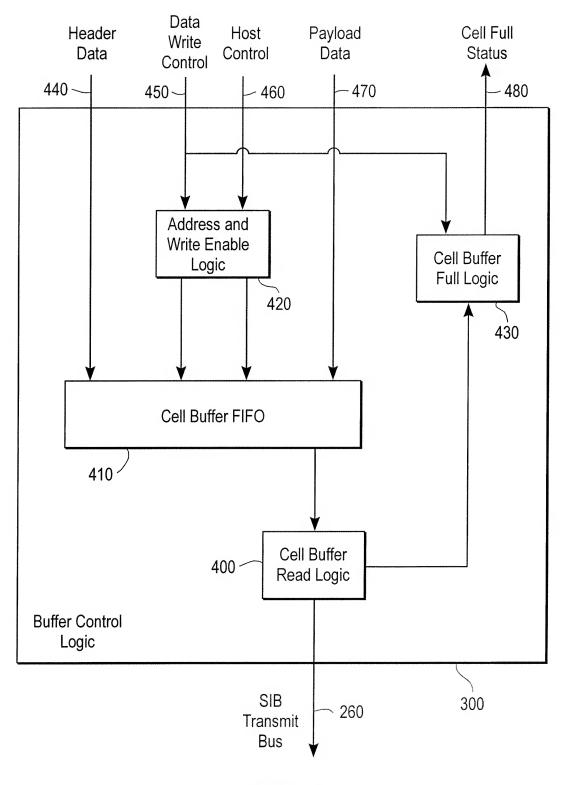


FIG. 3

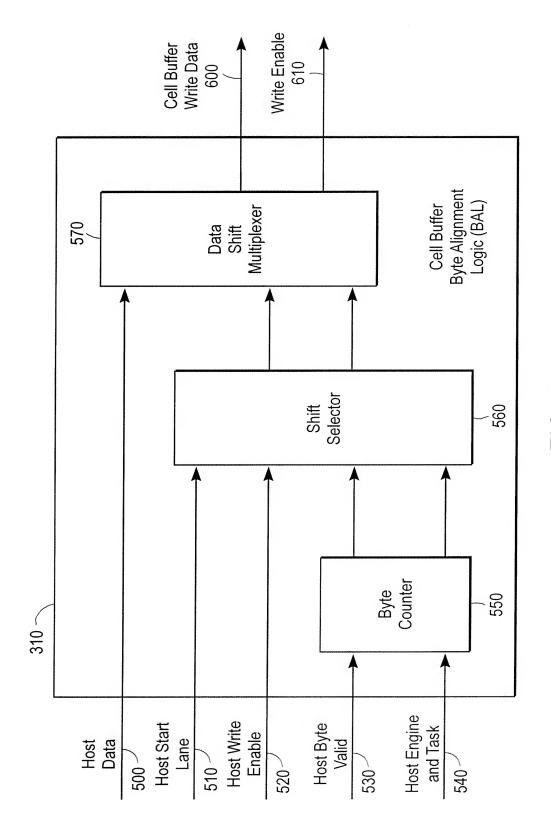


FIG. 4

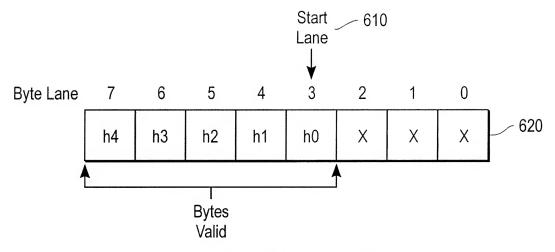


FIG. 5A Host Data

Byte Lane	7	6	5	4	3	2	1	0	
	Α	Α	Α	Α	٧	V	V	V	620
	А	Α	А	А	А	A	А	А	020

V = Contains Valid Date

A = This Byte Location Available

FIG. 5B Cell Buffer before Host Read

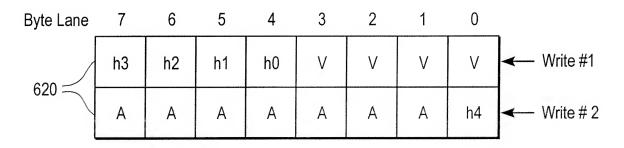
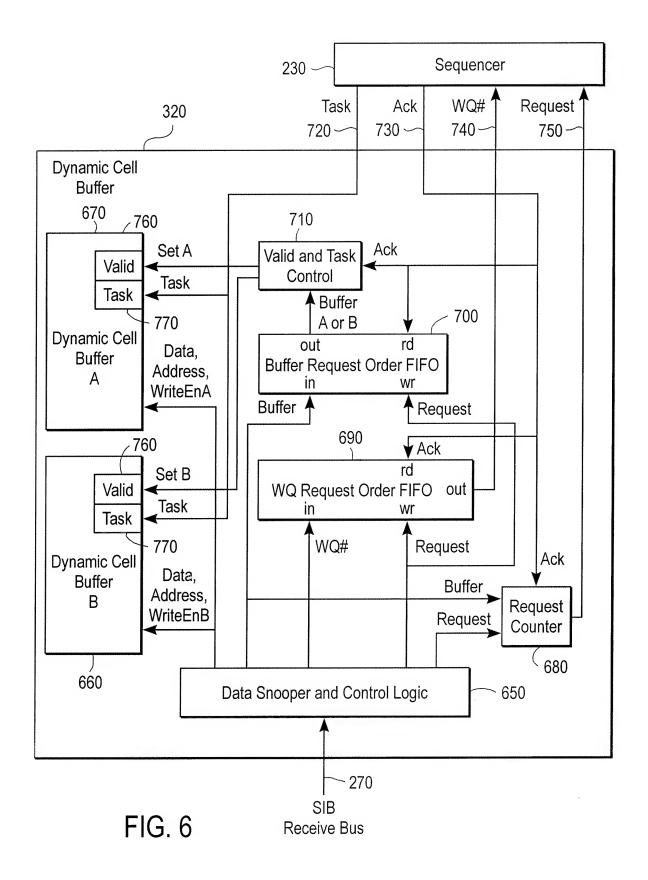


FIG. 5C Cell Buffer after Host Read



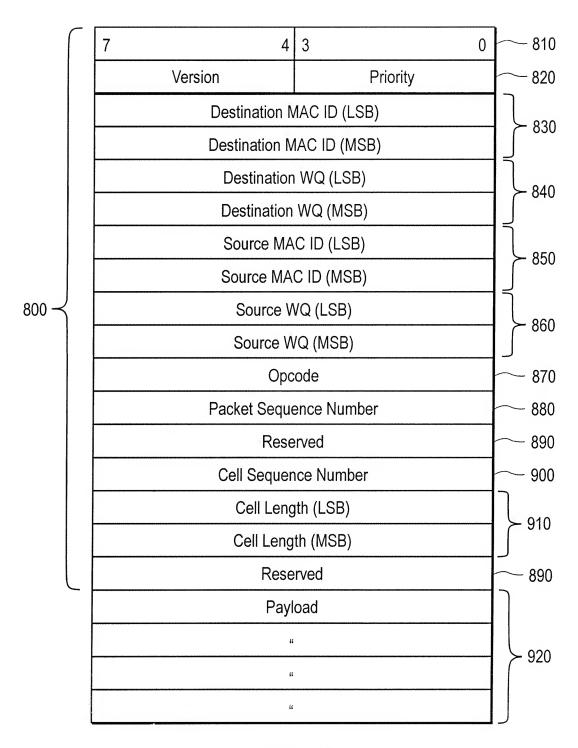


FIG. 7

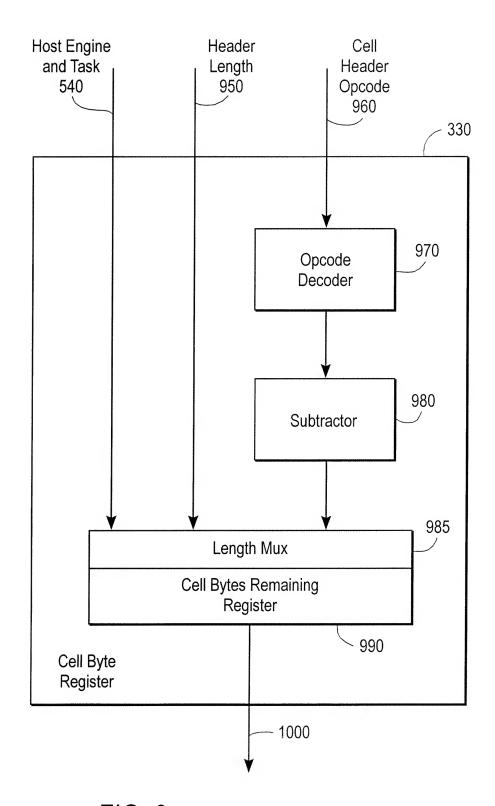


FIG. 8